

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	394	fuse and (multi-layered) and (single adj layer)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/26 15:45
S2	0	S1 and ((fuse adj circuit) with (plural or plurality))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/23 13:34
S3	8	S1 and (fuse adj circuit) and (plural or plurality)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/23 13:37
S4	0	S1 and (multi-layered adj fuse)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/23 13:37
S5	18	S1 and (multi-layered with fuse)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/23 13:37
S6	107	fuse with (single adj layer)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/23 14:02
S7	62	S6 and circuit	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/23 14:03
S8	0	fuse and ((multi-layered) with (single adj layer) with insulator)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/26 15:45
S9	2	fuse and ((multi adj layer) with (single adj layer) with insulator)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/26 15:46
S10	506	fuse and (multi-layered) and (single adj layer)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/26 15:49

S11	9136	fuse with semiconductor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/26 15:49
S12	60	fuse with semiconductor with ((multi or multiple or plural) adj layer)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/26 15:51
S13	871	257/529.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/26 16:22
S14	11	S13 and S10	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/26 15:52